



**PATENT**

Case Docket No. MICRON.079DV1C1

Date: March 18, 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s) : Gonzalez, et al.  
Appl. No. : 10/661,414  
Filed : September 12, 2003  
For : MULTIPLE THICKNESS GATE  
DIELECTRIC LAYERS  
Group Art Unit : 2826  
Examiner : Fetsum Abraham

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

March 18, 2005

(Date)

*Michael S. Okamoto*

Michael S. Okamoto, Reg. No. 47,831

**TRANSMITTAL LETTER**

**MAIL STOP ISSUE FEE**

**Commissioner for Patents**

**P.O. Box 1450**

**Alexandria, VA 22313-1450**

Dear Sir:

Enclosed for filing is the Issue Fee for the above-identified application:

- (X) Form PTOL-85 (in duplicate).
- (X) Information Disclosure Statement and PTO/SB/08 Equivalent in 3 total pages.
- (X) Fee Address Indication Form.
- (X) A check in the amount of \$1,709 to cover the issue fee, publication fee, and advanced order of copies is enclosed.
- (X) The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment, to Account No. 11-1410.
- (X) Return prepaid postcard.

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